

I THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Peter J. Hopper et al.

Application No.: 10/647,604

Filed: August 25, 2003

For: ULTRA LOW LEAKAGE MOSFET

TRANSISTOR

Confirmation No.: 2816

Group Art Unit: 2826

Examiner: Johannes P. Mondt

RESPONSE TO FINAL REJECTION MAILED SEPTEMBER 9, 2005

353 Sacramento Street, Suite 2200 San Francisco, CA 94111 Telephone: (415) 772-4900 Facsimile: (415) 398-2890

M/S RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on December 2, 2005.

STALLMAN & POLLOCK LLP

Dated: 12/ 9 /2005

Bv:

Tomicho Laurenco Cocaras

Sir:

In response to the Final Rejection mailed September 9, 2005, and in conjunction with Applicants' Request for Continuing Examination, please amend the above-identified application as follows:

Amendment to the Claims are provided in the listing of the claims that begins on page 2 of this paper.

Remarks begin on page 5 of this paper.

Atty Docket No.: NSC1-M3000 [P05657]